



Description

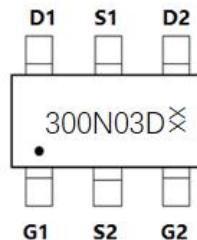
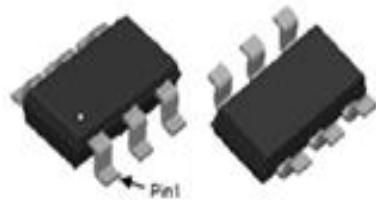
JMT Dual N-channel Enhancement Mode Power MOSFET

Features

- 30V,4.8A
- $R_{DS(ON)} < 33m\Omega$ @ $V_{GS} = 10V$
- $R_{DS(ON)} < 39m\Omega$ @ $V_{GS} = 4.5V$
- $R_{DS(ON)} < 57m\Omega$ @ $V_{GS} = 2.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

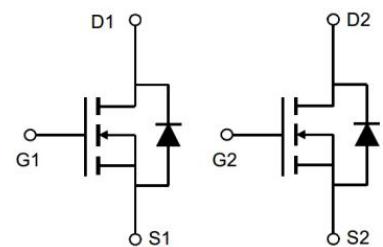
Application

- Load Switch
- PWM Application
- Power management



SOT-23-6L(Dual) top view

Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
300N03D	JMTM300N03D	TAPING	SOT-23-6L	7inch	3000	120000

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		± 12	V
I_D	Continuous Drain Current	$T_A = 25^\circ C$	4.8	A
		$T_A = 100^\circ C$	3.1	A
I_{DM}	Pulsed Drain Current ^{note1}		19	A
P_D	Power Dissipation	$T_A = 25^\circ C$	1.2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		104	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

**JMTM300N03D****Electrical Characteristics** ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V},$	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}= \pm 12\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	0.5	0.9	1.3	V
$R_{\text{DS}(\text{on})}$	Static Drain-Source on-Resistance note2	$V_{\text{GS}}=10\text{V}, I_D=4\text{A}$	-	25	33	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=3\text{A}$	-	28	39	
		$V_{\text{GS}}=2.5\text{V}, I_D=3\text{A}$	-	38	57	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	-	507	-	pF
C_{oss}	Output Capacitance		-	52	-	pF
C_{rss}	Reverse Transfer Capacitance		-	43	-	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=15\text{V}, I_D=4.8\text{A},$ $V_{\text{GS}}=4.5\text{V}$	-	9.1	-	nC
Q_{gs}	Gate-Source Charge		-	2.1	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	2.8	-	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=15\text{V}, R_{\text{GEN}}=3\Omega,$ $R_L=2.8\Omega, V_{\text{GS}}=10\text{V}$	-	3	-	ns
t_r	Turn-on Rise Time		-	2.8	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	25	-	ns
t_f	Turn-off Fall Time		-	4	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	4.8	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	19	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_s=4.8\text{A}$	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

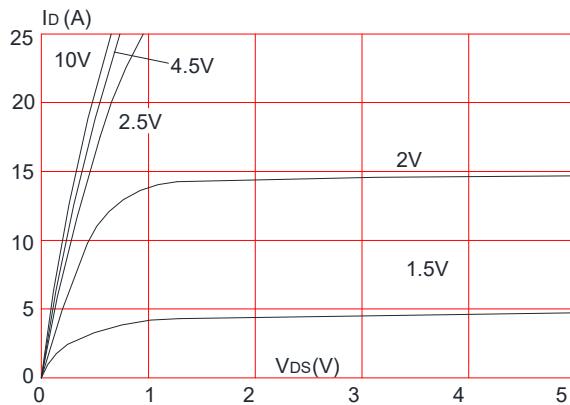


Figure 3: On-resistance vs. Drain Current

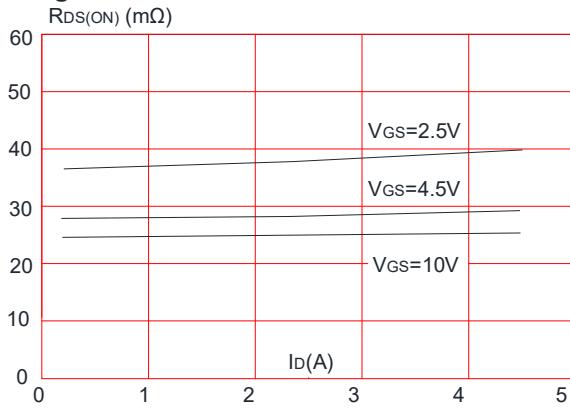


Figure 5: Gate Charge Characteristics

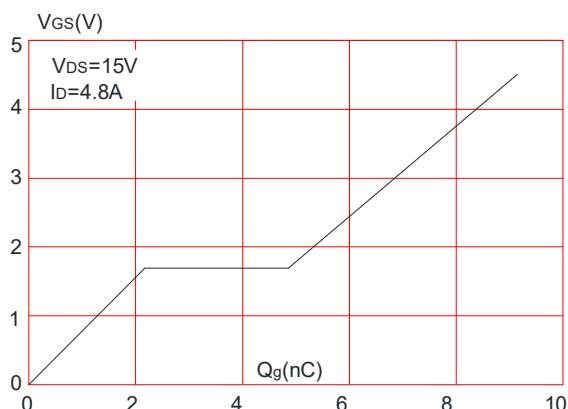


Figure 2: Typical Transfer Characteristics

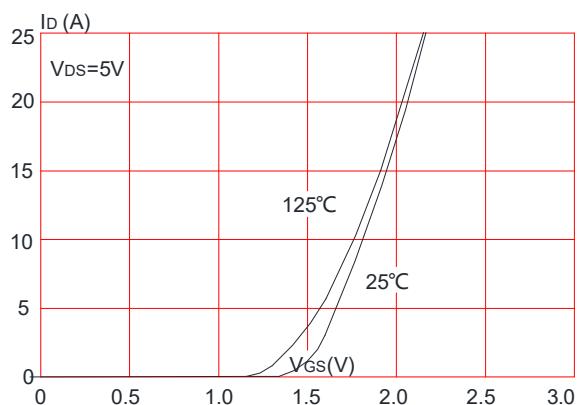


Figure 4: Body Diode Characteristics

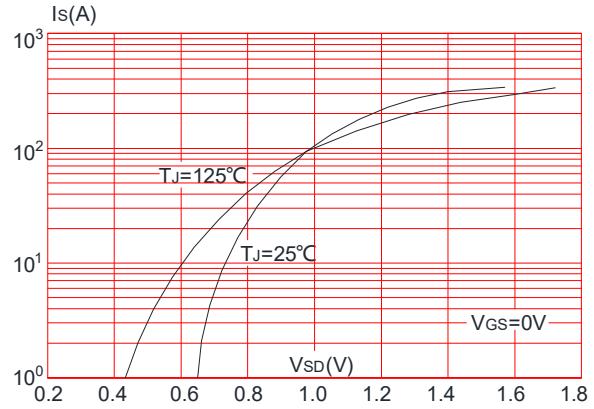


Figure 6: Capacitance Characteristics

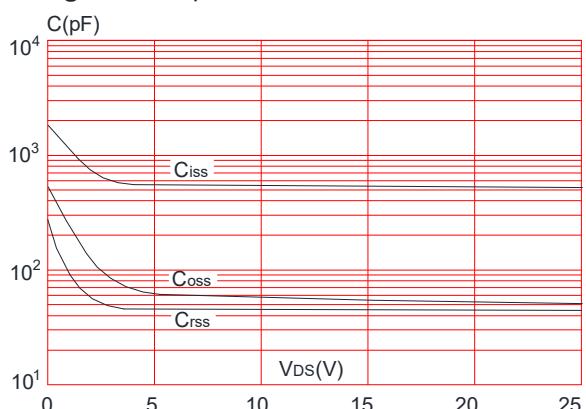


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

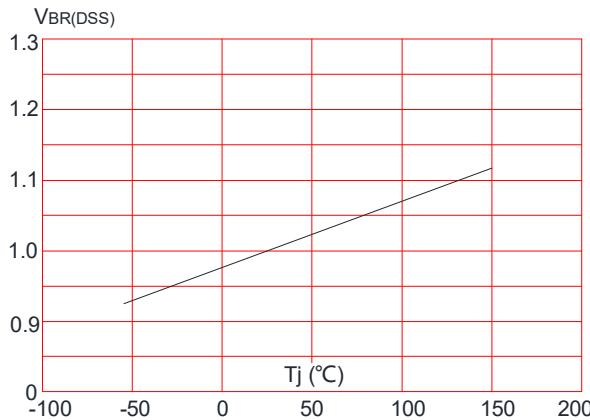


Figure 8: Normalized on Resistance vs. Junction Temperature

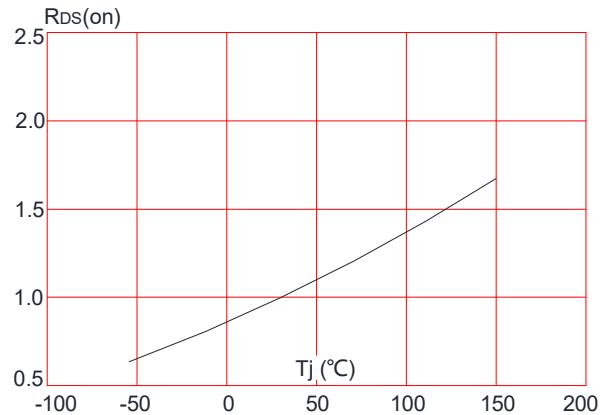


Figure 9: Maximum Safe Operating Area

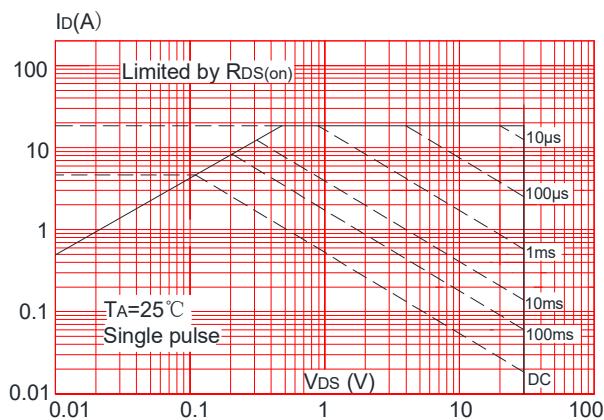


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

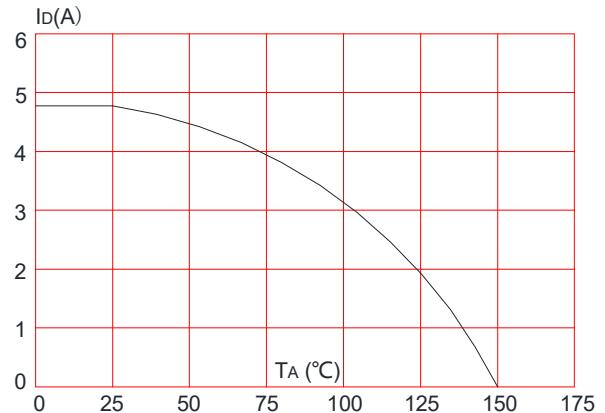
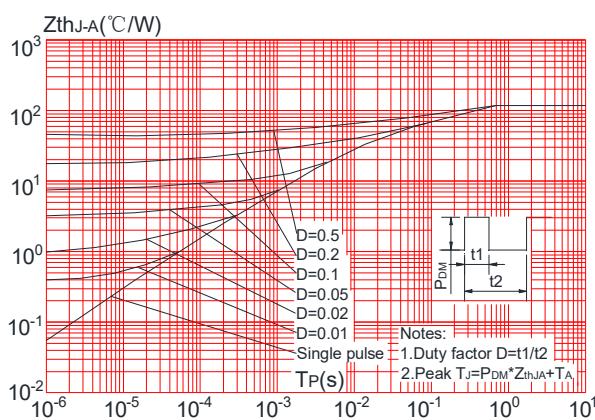


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Test Circuit

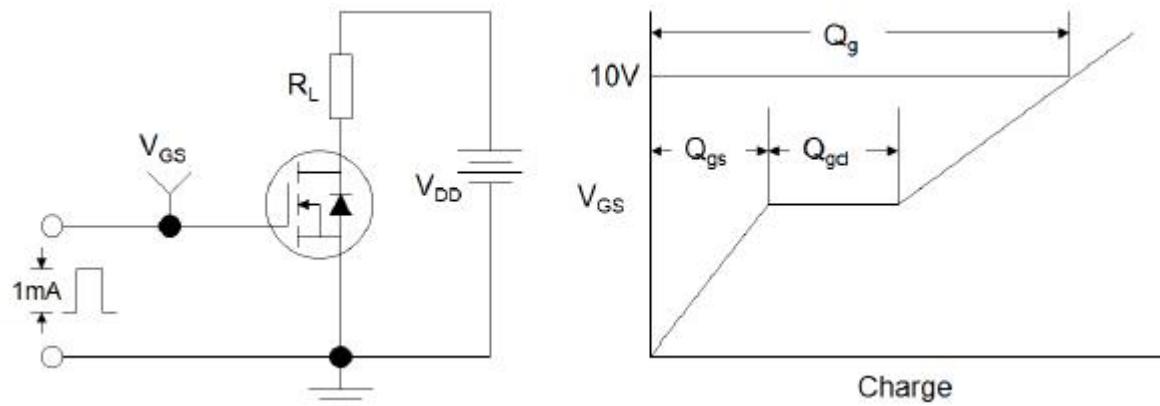


Figure 1: Gate Charge Test Circuit & Waveform

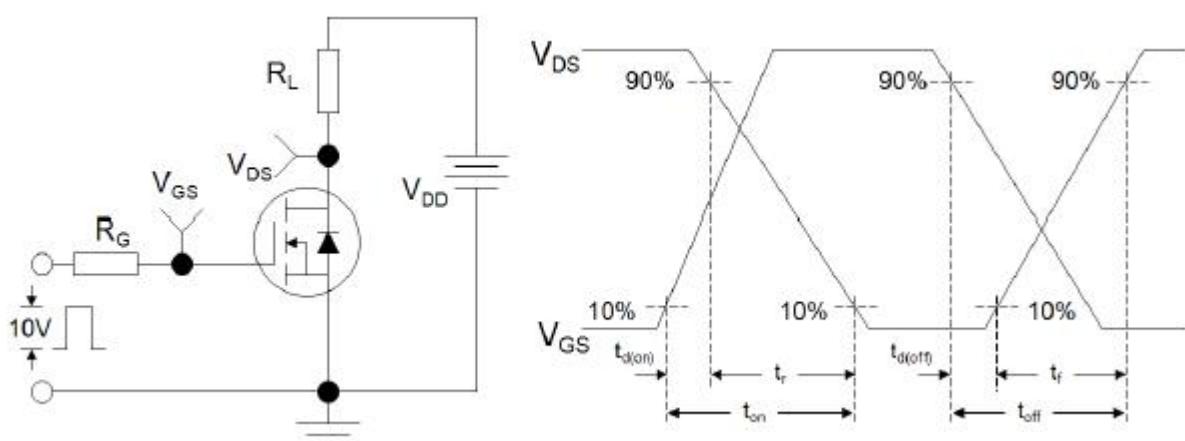


Figure 2: Resistive Switching Test Circuit & Waveforms

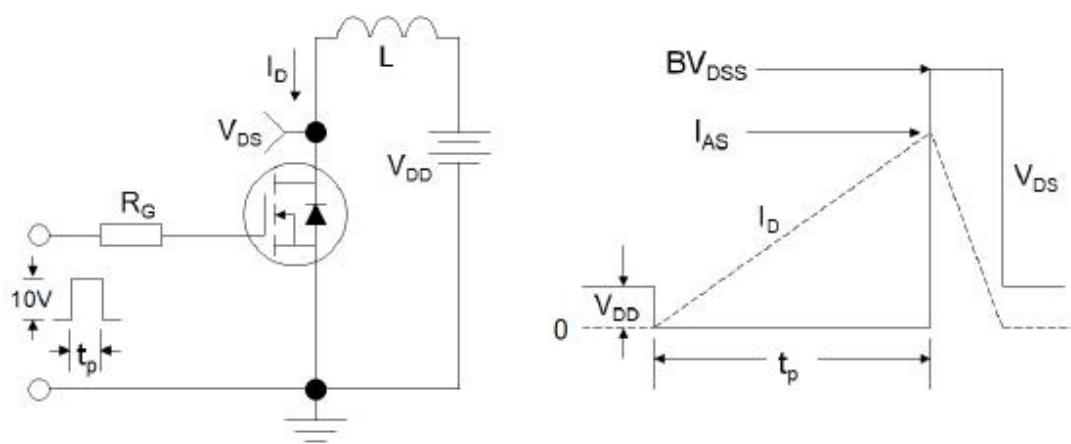
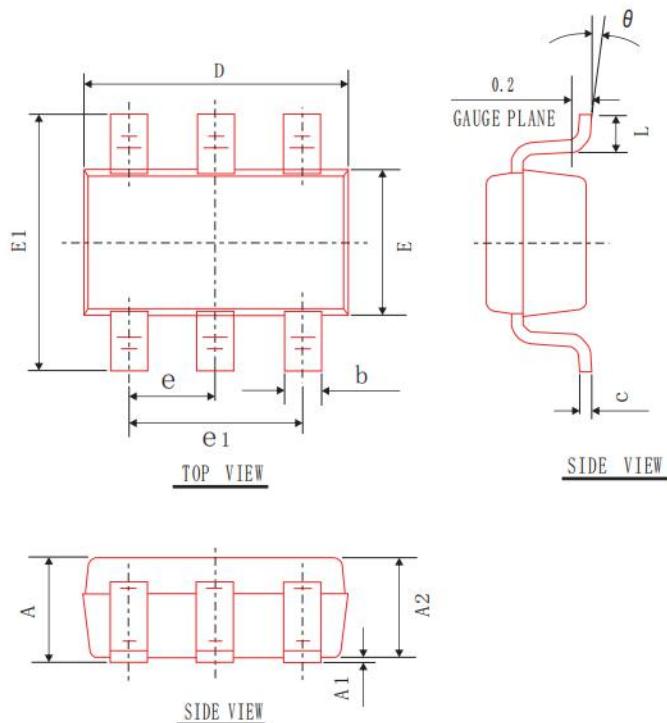


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms



Package Mechanical Data-SOT-23-6L

COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	—	—	1.20
A1	0.00	0.05	0.10
A2	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.119	0.127	0.135
e1	1.80	1.90	2.00
D	2.80	2.90	3.00
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
L	0.30	0.45	0.60
θ	0°	4°	8°
e	0.95BSC		

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